

MODEL PS12295 POWER SUPPLY

Engineering and Design Capabilities

Brandt engineers have over 100 years of combined power supply design experience and utilize the latest design software (e.g. AutoCAD, OrCAD, Mathcad, PSpice). With over 350 field-proven power supply designs, Brandt has a fully computerized and integrated research and development system and is experienced with all conducted/radiated emissions and conducted/radiated susceptibility levels of MIL-STD-461.

Facility

Brandt Electronics occupies a modern 21,000 square foot building located in Milpitas, CA, in the heart of Silicon Valley.

Quality Assurance

Brandt Electronics' Quality Assurance Program is in accordance with the most rigorous industry standards, such as ISO 9001:2008, MIL-I-45208, and Boeing's DI-9000 document. Our facility and procedures are regularly certified through DOD and prime contractor audits. Defect analysis with corrective action and follow-up and environmental stress screening are all included in our quality system.

Custom Design

Over 30% of our staff is dedicated to the development of new power supply designs. With over 350 models we have proven our ability to take your specs and turn them into successful and manufacturable designs.



Typical Operating Parameters

Input Voltages	115/220VAC, 3-phase, per MIL-STD-704A, 400Hz
Output Voltages	+5VDC @ 300A
Output Power	1500W
Efficiency	≥70% @ full rated power
Output Regulation	±1%
Output Ripple	50mV RMS @ 2400Hz
Protection	Short circuit – self recovering, over current – self recovering between 350A to 385A loads, over voltage – latching @ 5.95V / 300A

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Cross Reference

Brandt Electronics developed the PS12295 to be a direct replace for CEC Model 7681. We have shown our understanding of the technology that was originally used and made some improvements to bring this unit up to the current standards. This unit was only a part of the 4000 series of 1500W power supplies that CEC offered. Should your requirements include some of the other supplies in this family, Brandt would be happy to review your application.

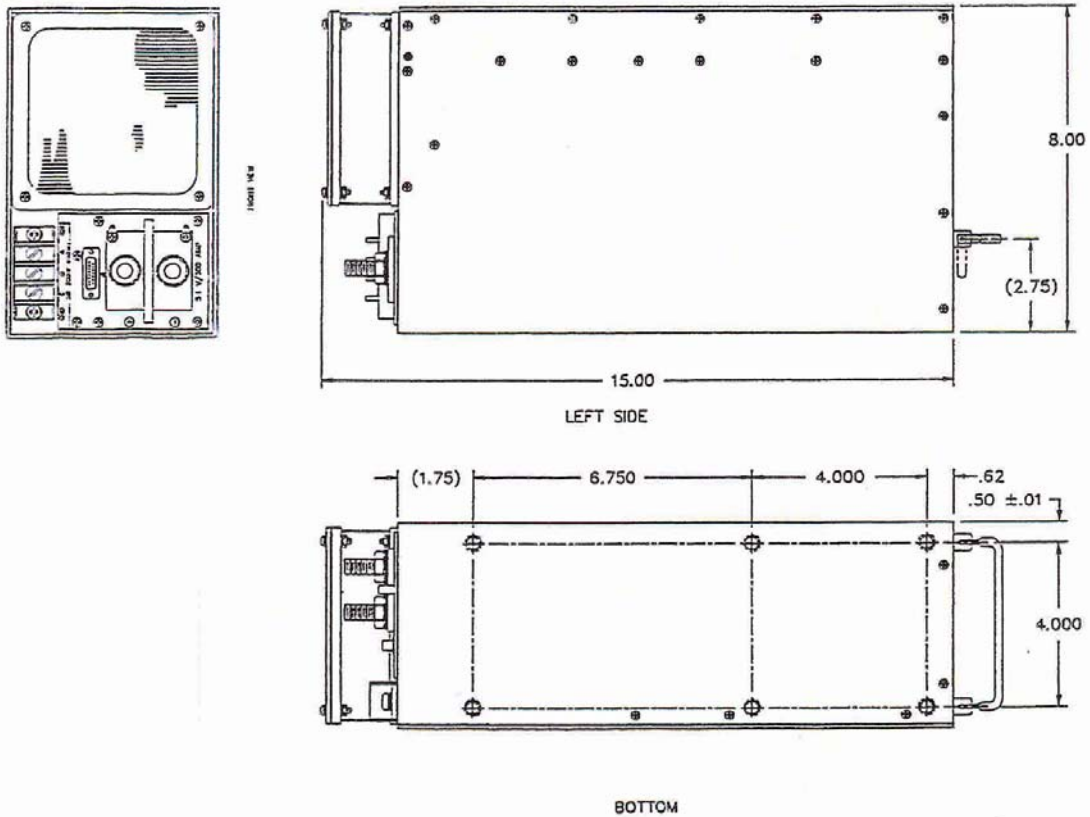


FIGURE 1 - OUTLINE DRAWING